

PATENT Docket No : NAUP0473USA

COMBINED DECLARATION AND POWER OF ATTORNEY

 $\ensuremath{^{\circ}}$ As a below named inventor , I hereby declare that :

I believe I am the sole (if only one name appears below), or joint (if more than one name

appears), original and first inventor of the subject matter which is claimed and for which is patent is sought on the invention entitled: "METHOD OF DIVIDING A SEMICONDUCTOR INTEGRATED CIRCUIT PATTERN"	
The specification for th	e above entitled invention was filed previously
with application serial number:	Filing Date:
	eviewed and understand the contents of the above identified as amended by any amendment referred to above.
Regulations, Section 1.56 (a). I fi application to disclose to the Patent to the patentability of the invention	plication in accordance with Title 37, Code of Federal urther acknowledge the daty in any continuation-in-part and Trademark Office all information known to be material a disclosed in this application, as defined in 1.56, which filling date of the prior application and the filling date of this
	PRIORITY CLAIM
There is no claim of prio	nity
Claim of priority is based	d on the following:
As a named inventor, I application and to transact all relate	WER OF ATTORNEY hereby appoint the following attorneies to prosecute this ed business in the Patent and Trademark Office: Registration# 41,526 WinstoalHsu@naipo.com.tw Registration# 50,478 MikeLewis@naipo.com.tw
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DECLARATION

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment or both under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validay of the application or any patters issued hereon.

Date: o7/o3/o2

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